0x0000 CCD_CDSREG Channel Writing to this register loads data to the addressed CDS channel 0x0003 CCD_DOPREG CCD_DOPREG Digital Output-Port; a 32-bit-medium-speed port 0x0003 CCD_SETIDENT Sets a mode that allows individual ADC channels to identify themselves by synthetic data generation 0x0004 CCD_BURST Command to burst send pixel data. Alternative to Pipeline write mode 0x0005 CCD_SEQTRIG Command to burst send pixel data. Alternative to Pipeline write mode 0x0108 CCD_ADCDATA Triggers the on board micro-sequencer to sequence the CDS switches during pixel acquisition 0x0200 CD_ADCCFG Twelve (12) read only registers that reflect raw ADC data 0x0210 CCD_ADCCFG mode of operation 0x0210 Access to twelve (12) DC offset DACs used to bring the ADC signal into the converters dynamic range 0x02210 Access to fourty-eight (48) voltage bias signal values 0x02250 CCD_HVBIASES Fourty-eight (48) configuration registers to establish the opporating mode of the telemetry ADCs 0x02300 CCD_AUXCFGREG Bit register to control auxiliary functions of the board 0x03301 CCD_AUXCFGREG Bit register to control auxiliary functions of the board 0x0310	Address	Function Name	Function Description
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0x0003 CCD_SETIDENT CCD_BURST CCD_BURST CCD_BURST CCD_BURST Triggers the on board micro-sequencer to sequence the CDS switches during pixel acquisition 0x0100 =>	00000	CCD_CDSREG	channel
CCD_SETIDENT Identify themselves by synthetic data generation	0x0001	CCD_DOPREG	Digital Output Port; a 32 bit medium speed port
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Name	0×0004		Command to burst send pixel data. Alternative to Pipeline
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